

- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

Product Summary

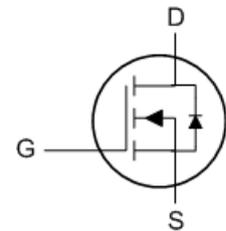
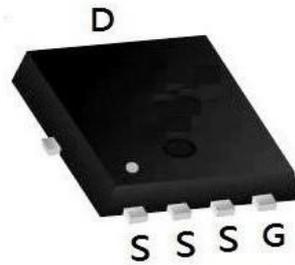


BVDSS	RDSON	ID
20V	2mΩ	120A

Description

PDFN5060-8L Pin Configuration

The XR120N02F is the high cell density trenched N-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications. The XR120N02F meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	20	V
V _{GS}	Gate-Source Voltage	± 12	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ^{1,6}	120	A
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ^{1,6}	76	A
I _{DM}	Pulsed Drain Current ²	480	A
EAS	Single Pulse Avalanche Energy ³	306	mJ
I _{AS}	Avalanche Current	---	A
P _D @T _C =25°C	Total Power Dissipation ⁴	69	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-Ambient ¹	---	---	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	1.8	°C/W

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	20	---	---	V
ΔBV _{DSS} /ΔT _J	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =1mA	---	---	---	V/ °C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =21A	---	---	---	mΩ
		V _{GS} =4.5V, I _D =21A	---	2	2.65	
		V _{GS} =2.5V, I _D =16A	---	2.5	3.27	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250μA	0.5	---	1	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	---	---	mV/ °C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =20V, V _{GS} =0V, T _J =25°C	---	---	1	μA
		V _{DS} =20V, V _{GS} =0V, T _J =125°C	---	---	100	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±12V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =21A	---	88	---	S
Q _g	Total Gate Charge	V _{DS} =10V, V _{GS} =4.5V, I _D =21A	---	70	---	nC
Q _{gs}	Gate-Source Charge		---	10	---	
Q _{gd}	Gate-Drain Charge		---	14	---	
T _{d(on)}	Turn-On Delay Time	V _{DS} =10V, V _{GS} =4.5V, R _G =3Ω, R _L =0.5Ω	---	8	---	ns
T _r	Rise Time		---	20	---	
T _{d(off)}	Turn-Off Delay Time		---	75	---	
T _f	Fall Time		---	82	---	
C _{iss}	Input Capacitance	V _{DS} =10V, V _{GS} =0V, f=1MHz	---	5670	---	pF
C _{oss}	Output Capacitance		---	460	---	
C _{rss}	Reverse Transfer Capacitance		---	416	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,4}	V _G =V _D =0V, Force Current	---	---	120	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =21A, T _J =25°C	---	---	1	V
t _{rr}	Reverse Recovery Time	I _F =21A, di/dt=100A/μs	---	15	---	nS
Q _{rr}	Reverse Recovery Charge	μs, T _J =25°C	---	6	---	nC

Note :

- 1..Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2.EAS condition: T_J=25°C, V_{DD}=10V, V_G=10V, R_G=25Ω, L=0.5mH.
- 3.Repetitive Rating: Pulse width limited by maximum junction temperature.e.

Typical Electrical And Thermal Characteristics (Curves)

Figure 1. Output Characteristics

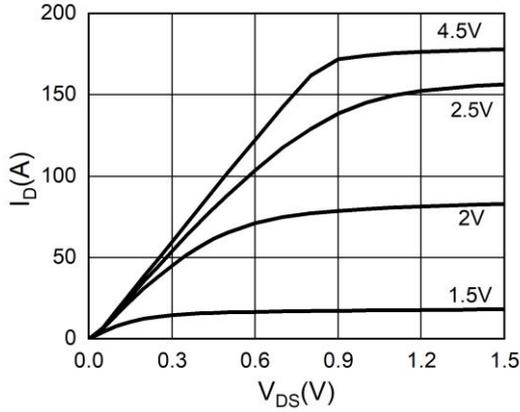


Figure 2. Transfer Characteristics

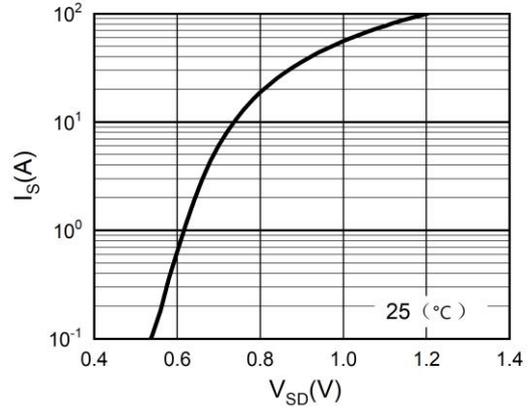


Figure 3. Power Dissipation

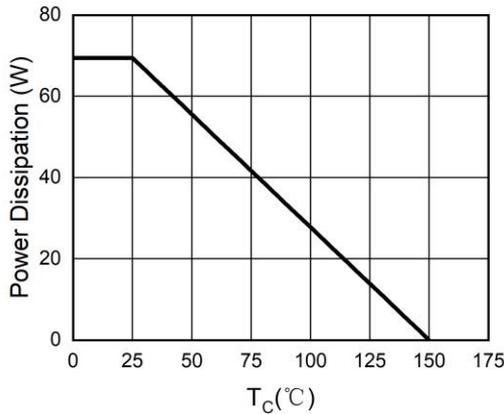


Figure 4. Drain Current

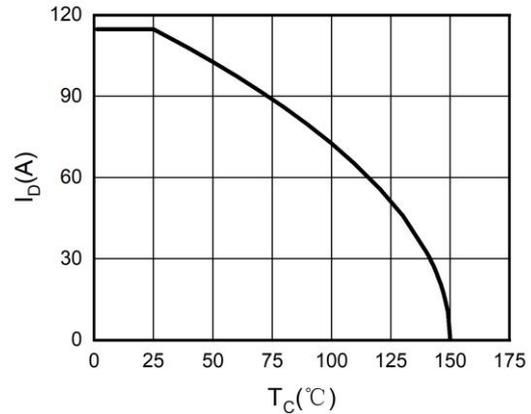


Figure 5. BVDS vs Junction Temperature

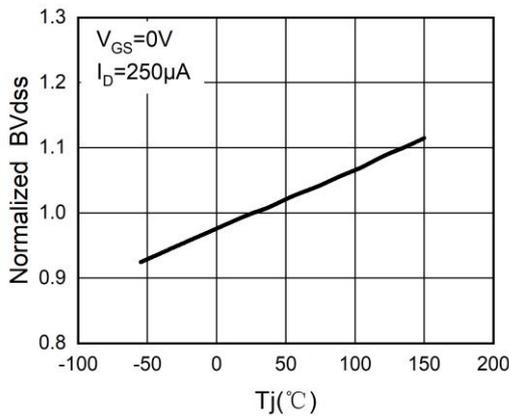
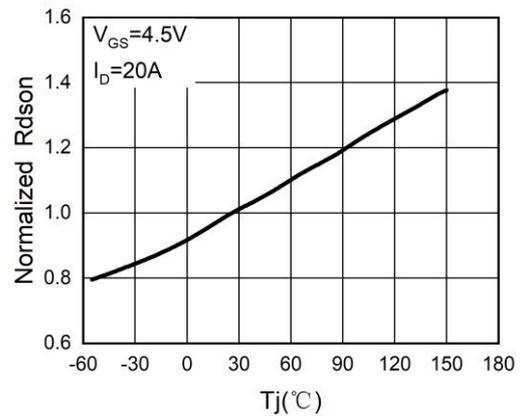


Figure 6. RDS(ON) vs Junction Temperature



Typical Electrical And Thermal Characteristics (Curves)

Figure 7. Gate Charge Waveforms

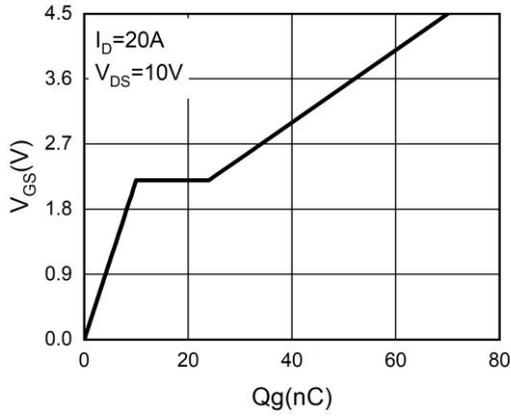


Figure 8. Capacitance

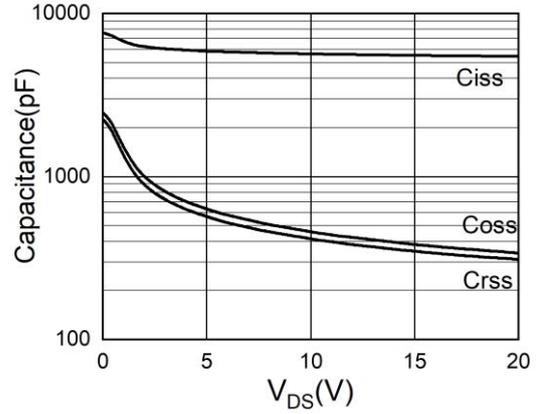


Figure 9. Body-Diode Characteristics

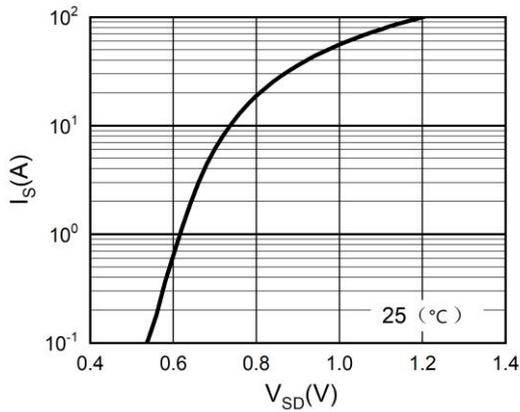
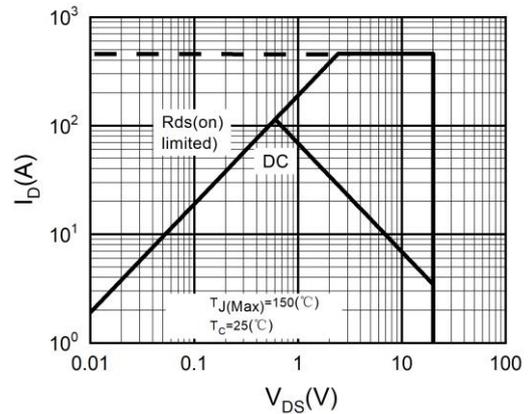


Figure 10. Maximum Safe Operating Area



Test Circuit

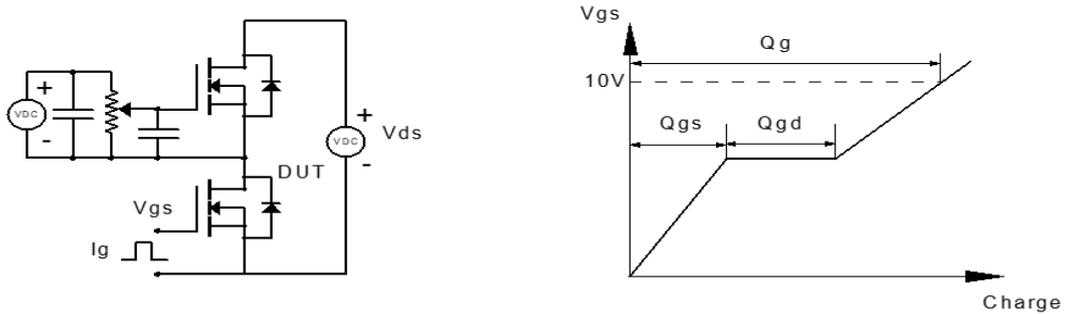


Figure 1: Gate Charge Test Circuit & Waveform

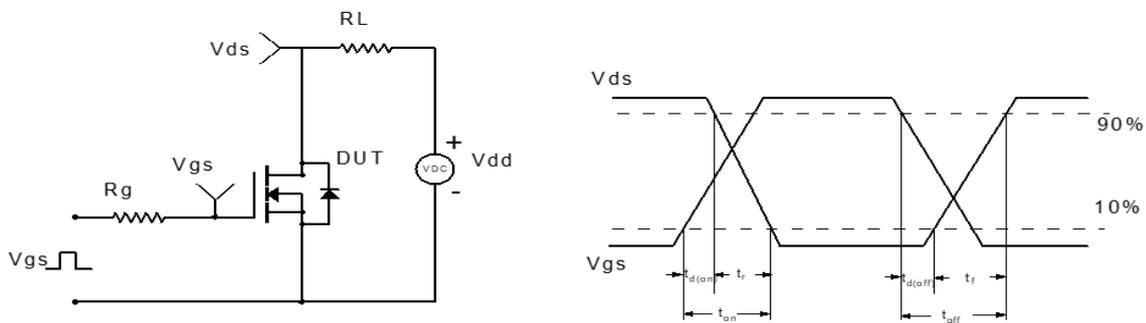


Figure 2: Resistive Switching Test Circuit & Waveform

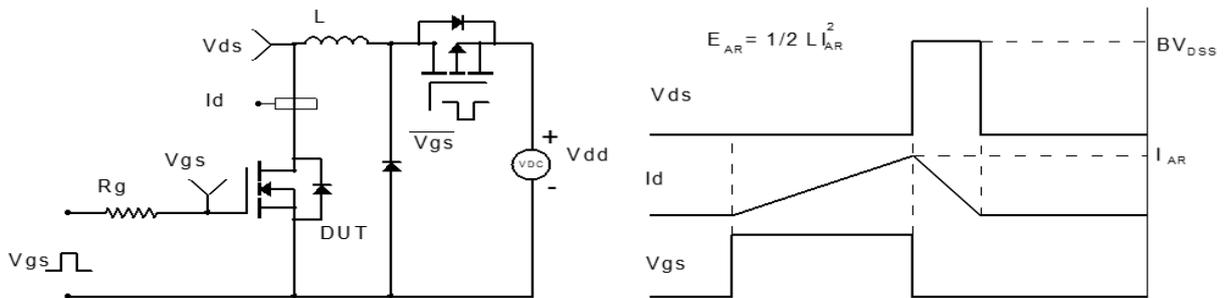


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

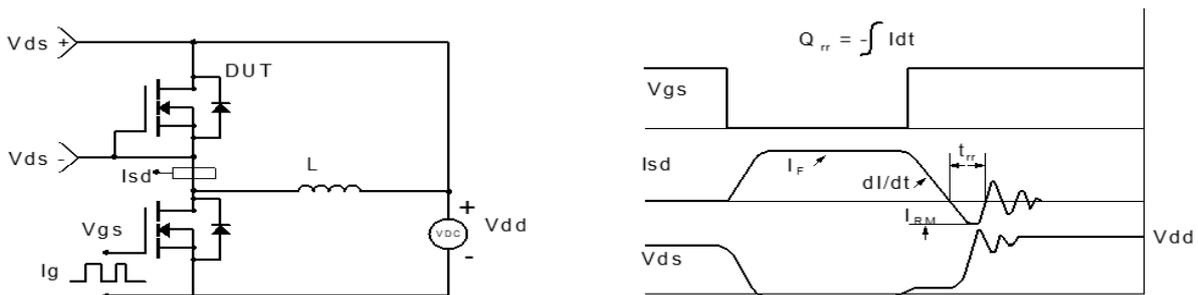
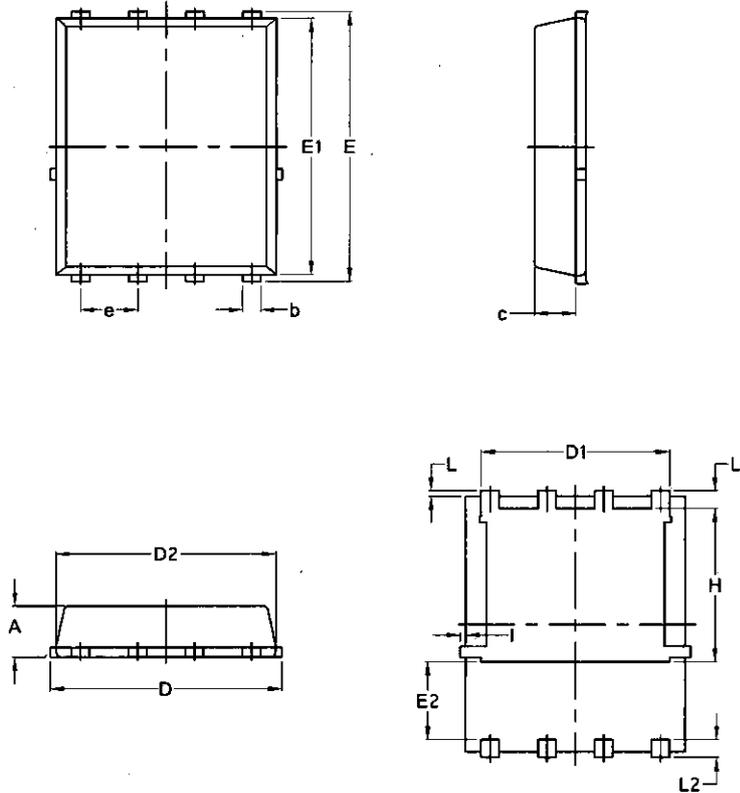


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data-PDFN5060-8L-Single



Symbol	Common			
	mm		Inch	
	Mim	Max	Min	Max
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.824	0.0970	0.0324	0.082
D	4.80	5.40	0.1890	0.2126
D1	4.11	4.31	0.1618	0.1697
D2	4.80	5.00	0.1890	0.1969
E	5.95	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	1.60	/	0.0630	/
e	1.27 BSC		0.05 BSC	
L	0.05	0.25	0.0020	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.50	0.0150	0.0197
H	3.30	3.50	0.1299	0.1378
I	/	0.18	/	0.0070