

Features

- Split Gate Trench MOSFET technology
- Excellent package for heat dissipation
- High density cell design for low $R_{DS(ON)}$

Applications

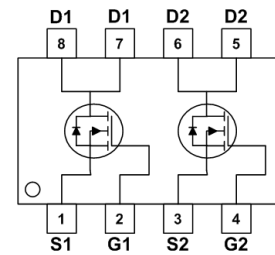
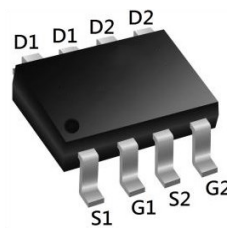
- DC-DC Converters
- Power management functions
- Synchronous-rectification applications

Product Summary



BVDSS	RDSON	ID
60V	12mΩ	15A

Dual SOP8 Pin Configuration



Absolute Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Max.	Units
V_{DSS}	Drain-Source Voltage	60	V
V_{GSS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current	$T_C = 25^\circ\text{C}$	15
		$T_C = 100^\circ\text{C}$	9
I_{DM}	Pulsed Drain Current ^{note1}	180	A
E_{AS}	Single Pulsed Avalanche Energy ^{note2}	36	mJ
P_D	Power Dissipation	$T_C = 25^\circ\text{C}$	60
$R_{\theta JC}$	Thermal Resistance, Junction to Case	2.5	$^\circ\text{C}/\text{W}$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +175	$^\circ\text{C}$

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	60	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=60V, V_{GS}=0V,$	-	-	1.0	μA
I_{GSS}	Gate to Body Leakage Current	$V_{DS}=0V, V_{GS}=\pm 20V$	-	-	± 100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	1.6	2.5	V
$R_{DS(on)}$	Static Drain-Source on-Resistance <small>note3</small>	$V_{GS}=10V, I_D=20A$	-	12	15	m Ω
		$V_{GS}=4.5V, I_D=10A$	-	15	20	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=25V, V_{GS}=0V,$ $f=1.0MHz$	-	930	-	pF
C_{oss}	Output Capacitance		-	230	-	pF
C_{rss}	Reverse Transfer Capacitance		-	8	-	pF
Q_g	Total Gate Charge	$V_{DS}=30V, I_D=20A,$ $V_{GS}=10V$	-	22	-	nC
Q_{gs}	Gate-Source Charge		-	4.5	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	3.5	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD}=30V, I_D=20A,$ $R_G=1.6\Omega, V_{GS}=10V$	-	4.5	-	ns
t_r	Turn-on Rise Time		-	2.7	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	13.8	-	ns
t_f	Turn-off Fall Time		-	2.7	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	15	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	180	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS}=0V, I_S=30A$	-	-	1.2	V
t_{rr}	Body Diode Reverse Recovery Time	$T_J=25^\circ\text{C},$ $I_F=20A, di/dt=100A/\mu s$	-	18	-	ns
Q_{rr}	Body Diode Reverse Recovery Charge		-	12	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

 2. EAS condition: $T_J=25^\circ\text{C}, V_{DD}=30V, V_G=10V, R_G=25\Omega, L=0.5mH, I_{AS}=12A$

 3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 0.5\%$

Typical Performance Characteristics

Figure 1: Output Characteristics

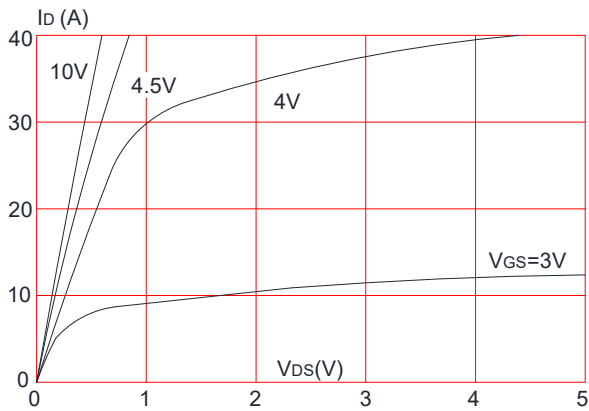


Figure 2: Typical Transfer Characteristics

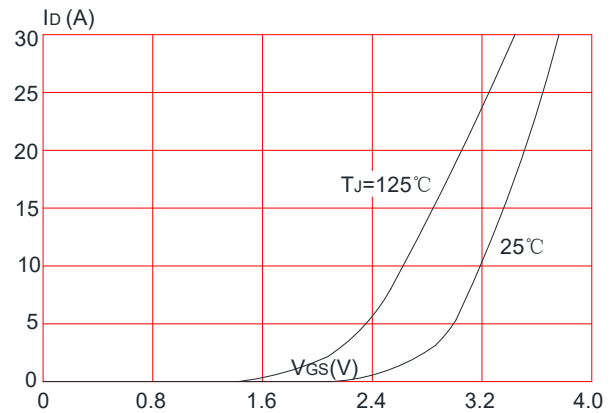


Figure 3: On-resistance vs. Drain Current

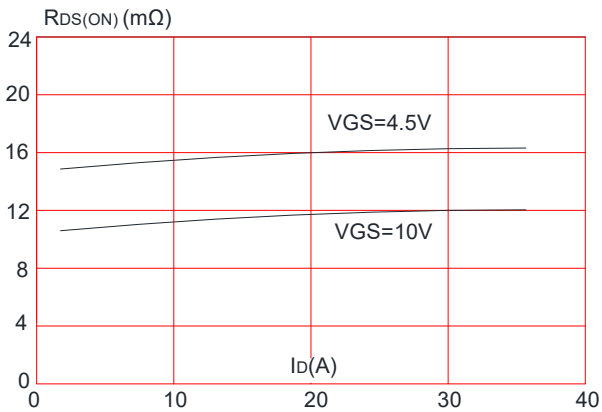


Figure 4: Body Diode Characteristics

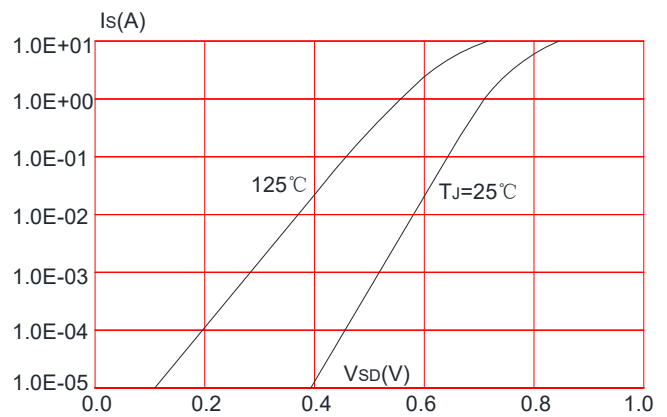


Figure 5: Gate Charge Characteristics

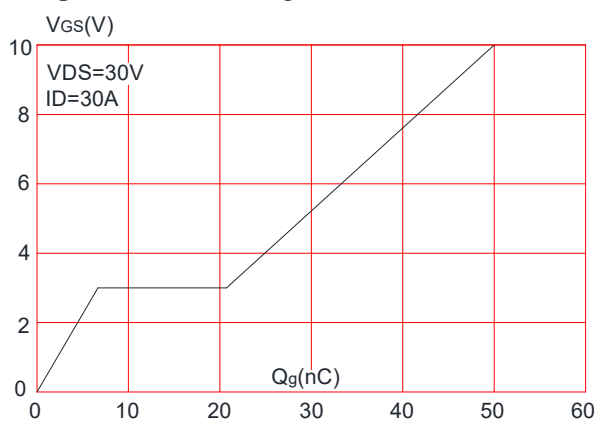


Figure 6: Capacitance Characteristics

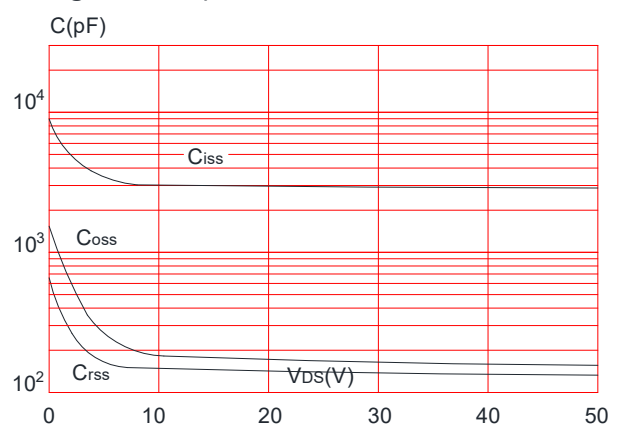


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

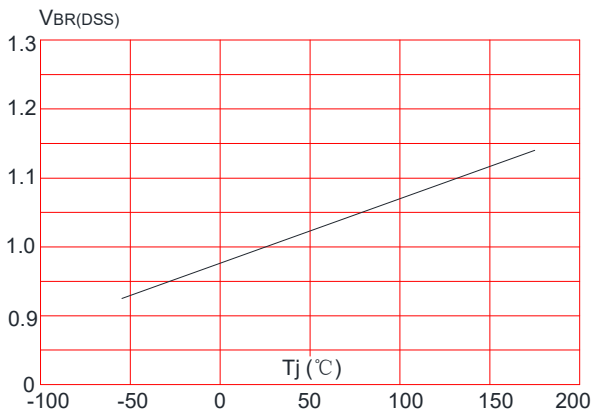


Figure 8: Normalized on Resistance vs. Junction Temperature

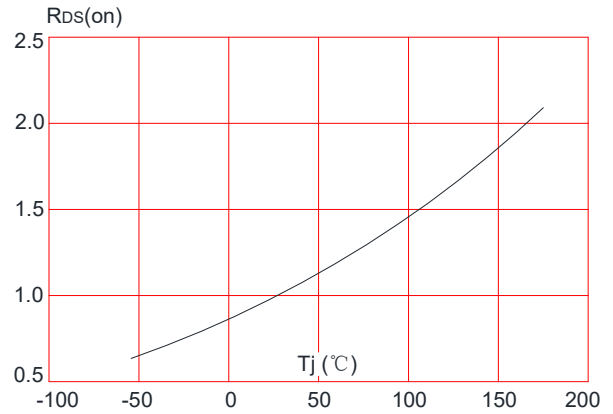


Figure 9: Maximum Safe Operating Area

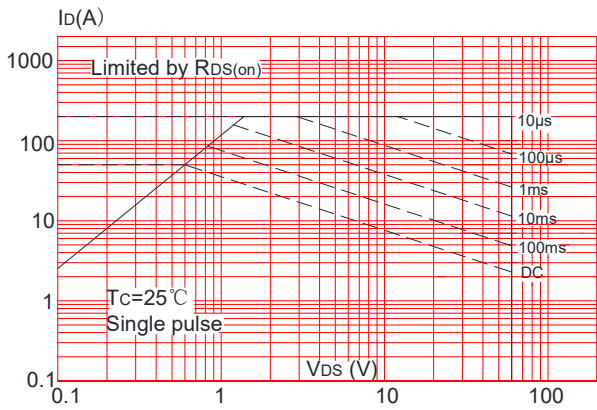


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

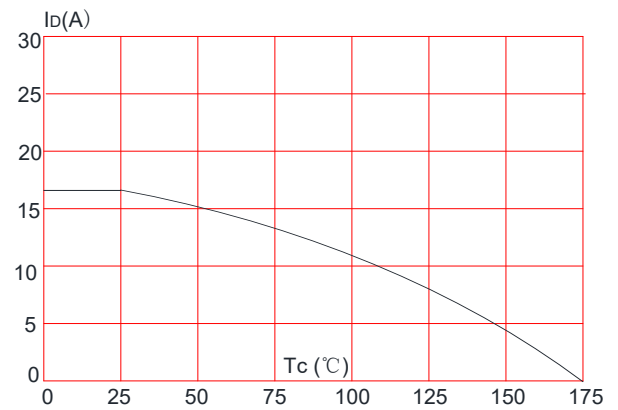
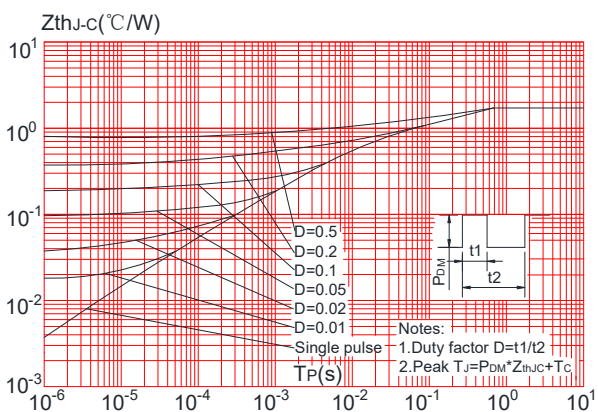
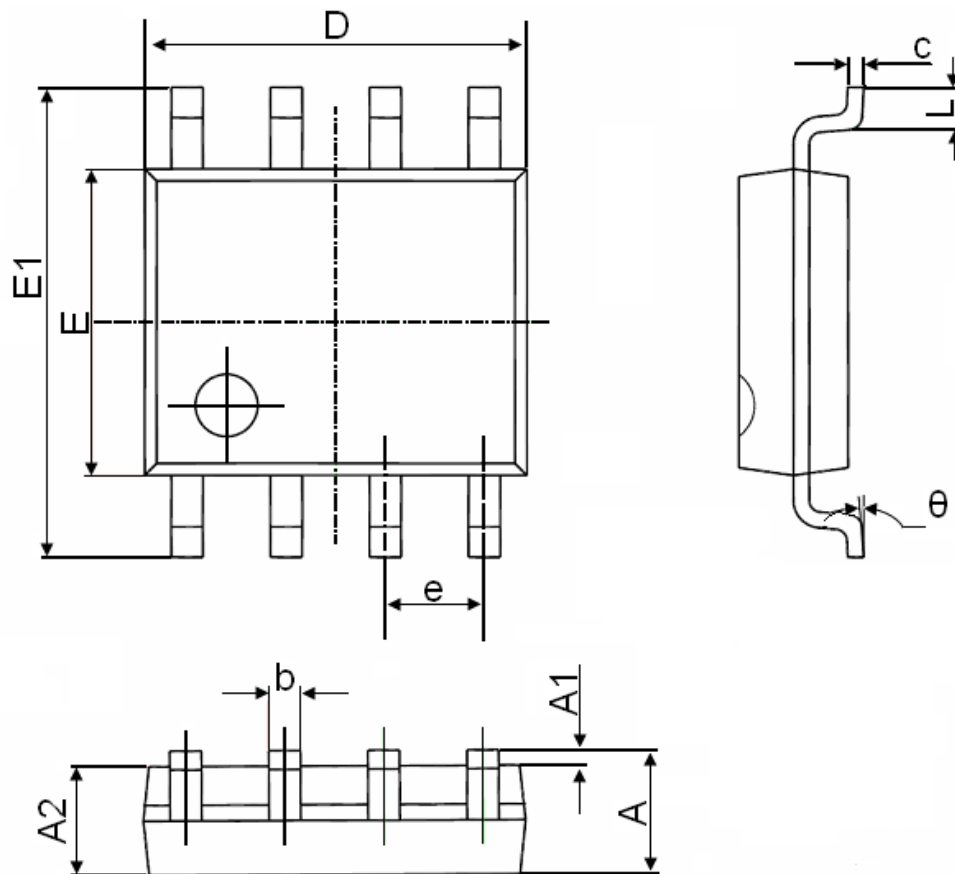


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case



Package Mechanical Data-SOP-8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°