

N-Ch and P-Ch Fast Switching MOSFETs

- ★ 100% EAS Guaranteed
- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology



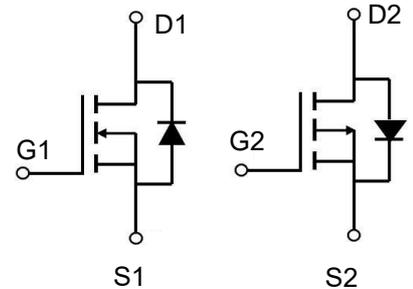
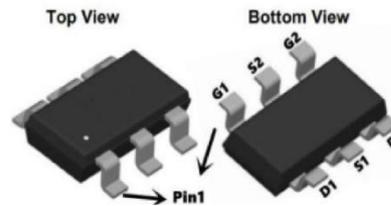
Product Summary

BVDSS	RDSON	ID
40V	26mΩ	5A
-40V	60mΩ	-5.5A

Description

The XR6G04L is the high performance complementary N-ch and P-ch MOSFETs with high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications. The XR6G04L meet the RoHS and Green Product requirement 100% EAS guaranteed with full function reliability approved.

SOT 23-6L Pin Configurations



Applications

- Power management in half bridge and inverters
- DC-DC Converter
- Load Switch

Absolute Maximum Ratings

Symbol	Parameter	Rating		Units
		N-Ch	P-Ch	
V_{DS}	Drain-Source Voltage	40	-40	V
V_{GS}	Gate-Source Voltage	± 20	± 20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	5	-4	A
$I_D@T_C=100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	3.6	-3.2	A
I_{DM}	Pulsed Drain Current ²	14.5	-15	A
EAS	Single Pulse Avalanche Energy ³	28	26	mJ
I_{AS}	Avalanche Current	10.8	-7.2	A
$P_D@T_C=25^\circ C$	Total Power Dissipation ⁴	2.5	2.1	W
T_{STG}	Storage Temperature Range	-55 to 150	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	-55 to 150	$^\circ C$

N-Ch and P-Ch Fast Switching MOSFETs

Electrical Characteristics (T_J=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	40	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =40V, V _{GS} =0V,	-	-	1.0	μA
I _{GSS}	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} = ±20V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1.0	1.5	2.2	V
R _{DS(on)}	Static Drain-Source on-Resistance <small>note3</small>	V _{GS} =10V, I _D =4A	-	26	40	mΩ
		V _{GS} =4.5V, I _D =3A	-	35	60	
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =20V, V _{GS} =0V, f=1.0MHz	-	435	-	pF
C _{oss}	Output Capacitance		-	58	-	pF
C _{rss}	Reverse Transfer Capacitance		-	35	-	pF
Q _g	Total Gate Charge	V _{DS} =20V, I _D =3A, V _{GS} =10V	-	11	-	nC
Q _{gs}	Gate-Source Charge		-	2	-	nC
Q _{gd}	Gate-Drain("Miller") Charge		-	2.5	-	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DD} =20V, I _D =4A, R _L =1Ω, R _{GEN} =3Ω, V _{GS} =10V	-	10	-	ns
t _r	Turn-on Rise Time		-	8	-	ns
t _{d(off)}	Turn-off Delay Time		-	29	-	ns
t _f	Turn-off Fall Time		-	12	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	5	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	20	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} =0V, I _S =5A	-	-	1.2	V
t _{rr}	Body Diode Reverse Recovery Time	T _J =25°C, I _F =5A, di/dt=100A/μs	-	20	-	ns
Q _{rr}	Body Diode Reverse Recovery Charge		-	11	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

Electrical Characteristics (T_J=25°C unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Static Characteristics						
Drain-Source Breakdown Voltage	V_{(BR)DSS}	V _{GS} = 0V, I _D = -250μA	-40	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V _{DS} = -40V, V _{GS} = 0V	-	-	-1	μA
Gate-Body Leakage	I_{GSS}	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
Gate-Threshold Voltage	V_{GS(th)}	V _{DS} = V _{GS} , I _D = -250μA	-1.2	-1.5	-2.5	V
Drain-Source on-Resistance ³	R_{DS(on)}	V _{GS} = -10V, I _D = -5A	-	60	80	mΩ
		V _{GS} = -4.5V, I _D = -4A	-	71	92	
Dynamic Characteristics⁴						
Input Capacitance	C_{iss}	V _{GS} = 0V, V _{DS} = -20V, f=1.0MHz	-	553	-	pF
Output Capacitance	C_{oss}		-	50	-	
Reverse Transfer Capacitance	C_{rss}		-	42	-	
Switching Characteristics⁴						
Total Gate Charge	Q_g	V _{GS} = -10V, V _{DS} = -20V, I _D = -5A	-	11.8	-	nC
Gate-Source Charge	Q_{gs}		-	2.2	-	
Gate-Drain Charge	Q_{gd}		-	3	-	
Turn-on Delay Time	t_{d(on)}	V _{DS} = -20V, V _{GS} = -10V R _L = 2.5Ω, R _G = 3Ω	-	7	-	ns
Rise Time	t_r		-	6.5	-	
Turn-off Delay Time	t_{d(off)}		-	24	-	
Fall Time	t_f		-	7.8	-	
Drain-Source Body Diode Characteristics						
Body Diode voltage ³	V_{DS}	I _S = -5A, V _{GS} =0V	-	-	-1.2	V
Continuous Source Current	I_S		-	-	-5.5	A

Notes:

1. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C.
2. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
3. Pulse Test: Pulse width≤300μs, duty cycle≤2%.
4. This value is guaranteed by design hence it is not included in the production test.

Typical Performance Characteristics-N

Figure 1: Output Characteristics

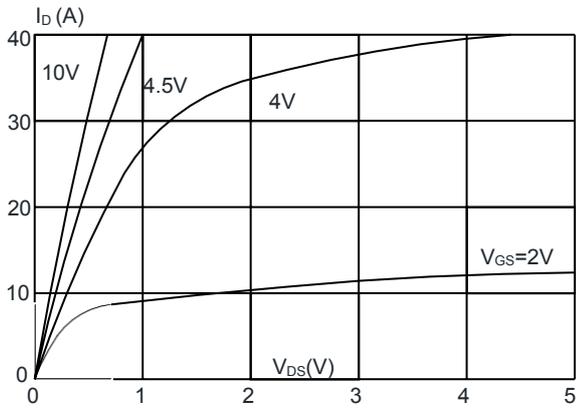


Figure 2: Typical Transfer Characteristics

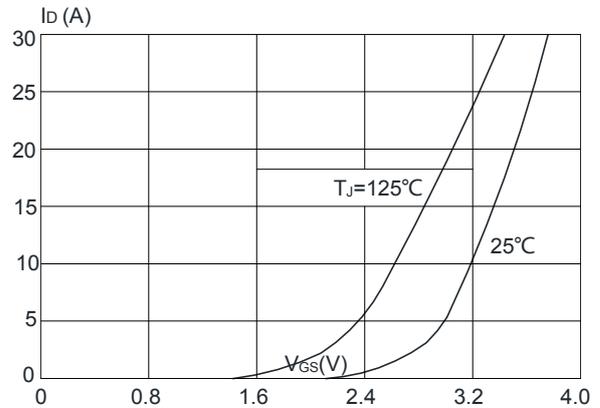


Figure 3: On-resistance vs. Drain Current

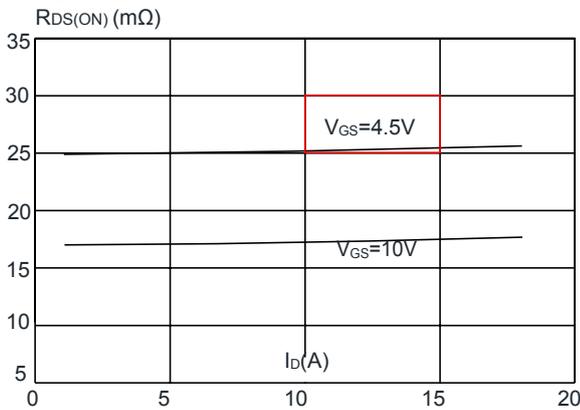


Figure 4: Body Diode Characteristics

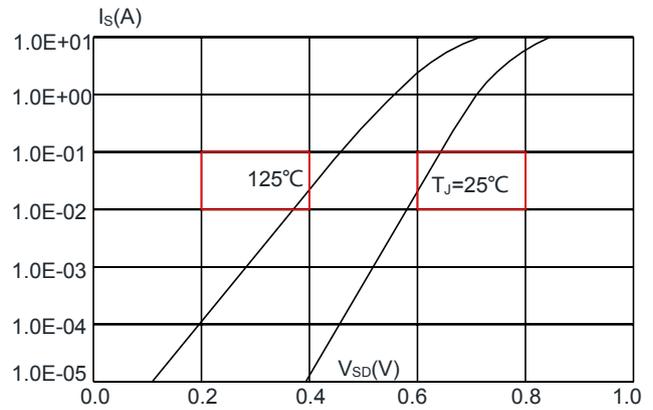


Figure 5: Gate Charge Characteristics

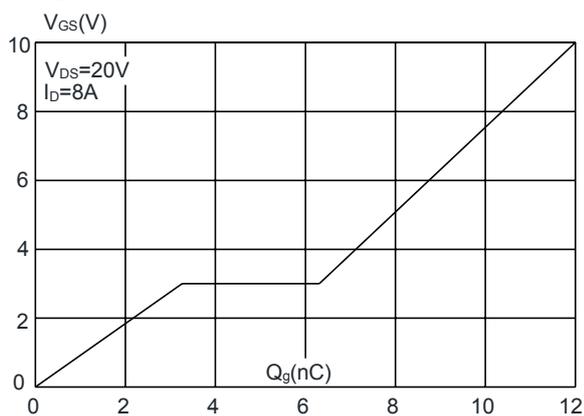
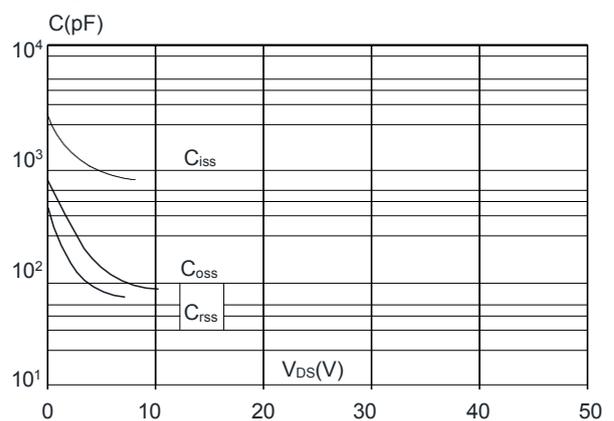


Figure 6: Capacitance Characteristics



N-Ch and P-Ch Fast Switching MOSFETs

Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

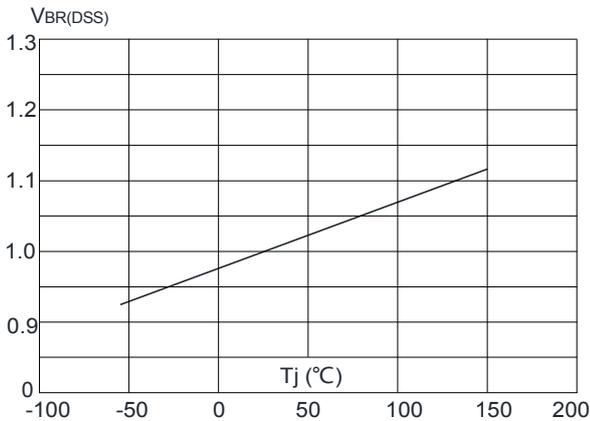


Figure 8: Normalized on Resistance vs. Junction Temperature

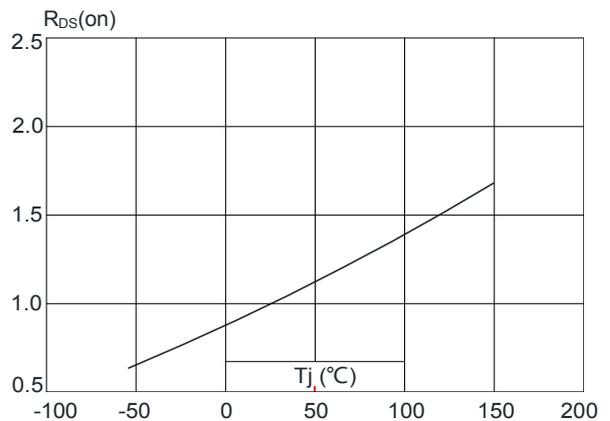


Figure 9: Maximum Safe Operating Area

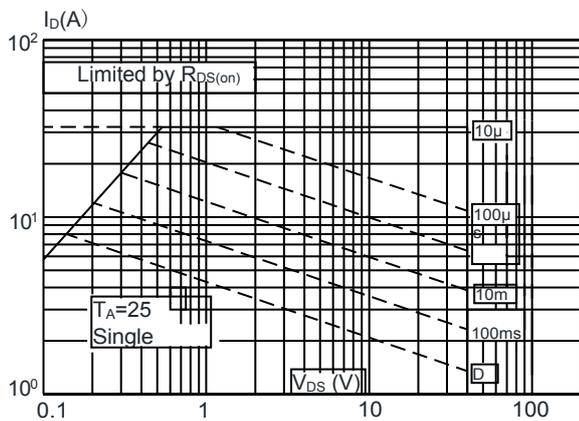


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

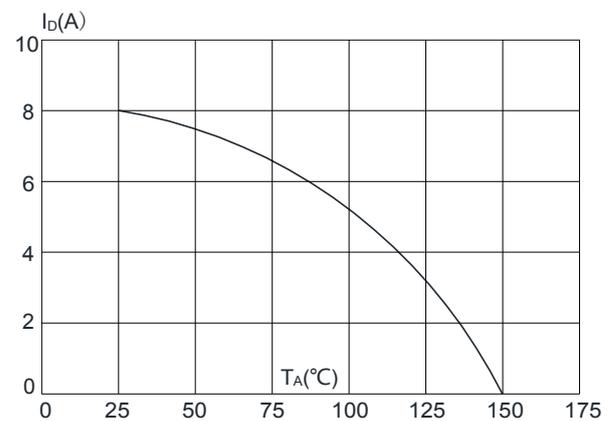
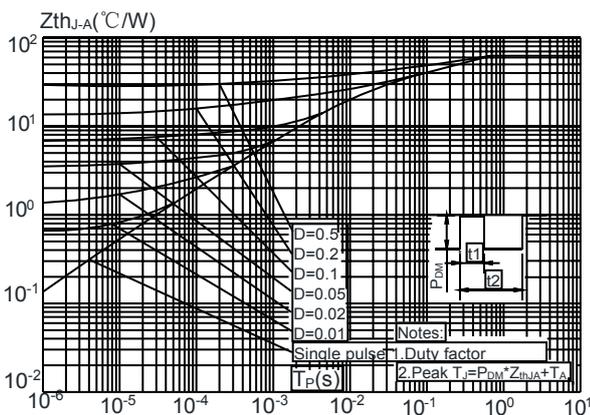


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



Typical Performance Characteristics-P

Figure 1: Output Characteristics

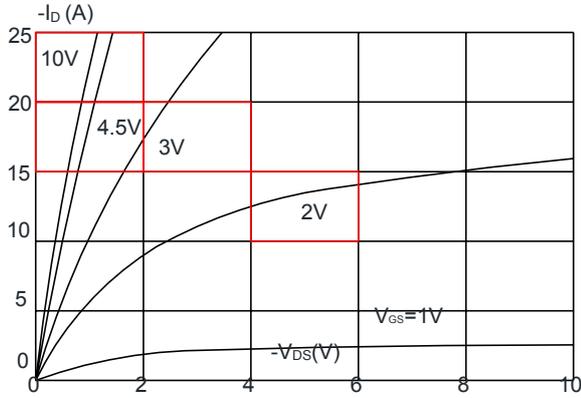


Figure 2: Typical Transfer Characteristics

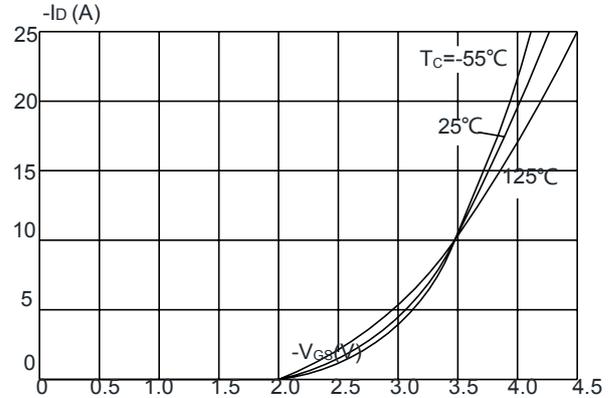


Figure 3: On-resistance vs. Drain Current

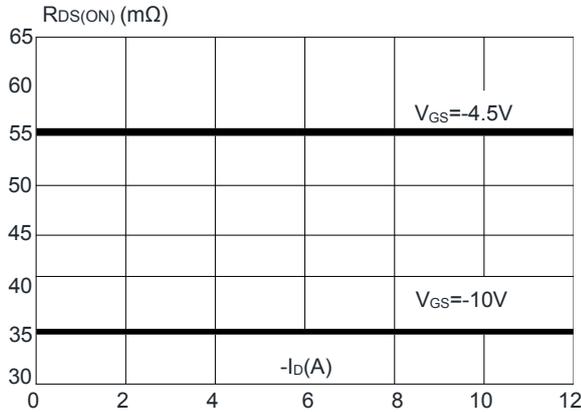


Figure 4: Body Diode Characteristics

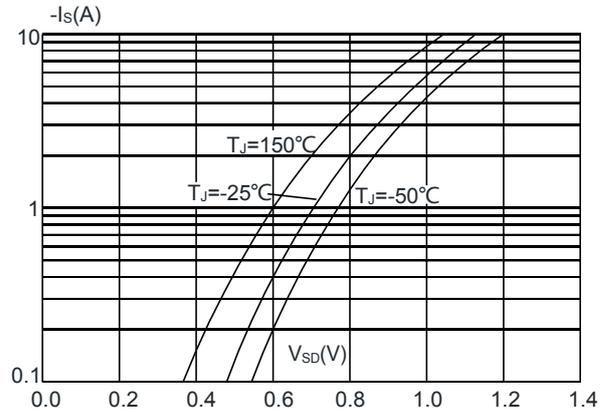


Figure 5: Gate Charge Characteristics

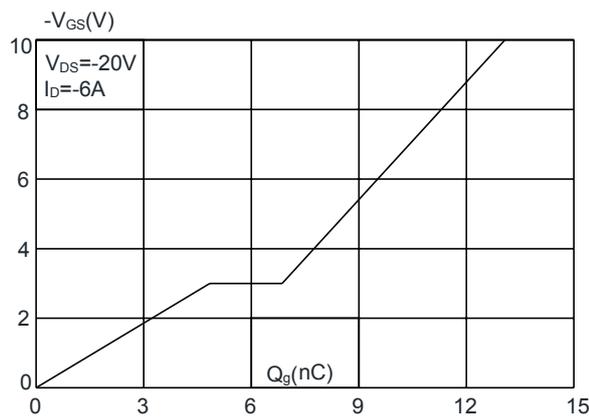
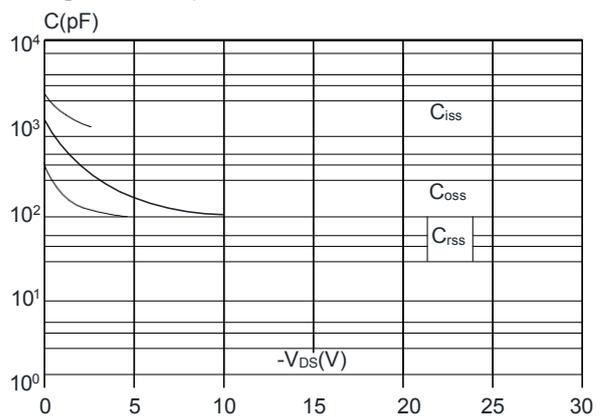


Figure 6: Capacitance Characteristics



N-Ch and P-Ch Fast Switching MOSFETs

Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

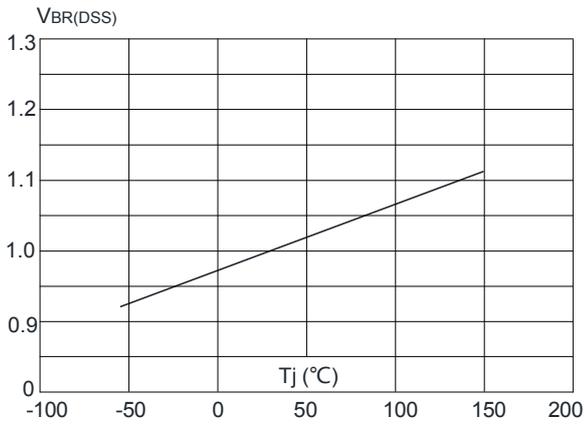


Figure 8: Normalized on Resistance vs. Junction Temperature

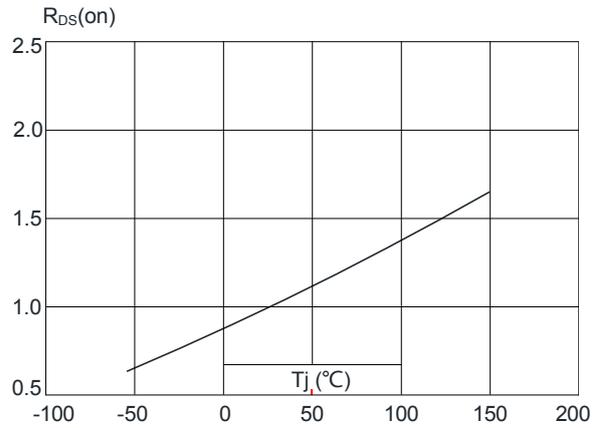


Figure 9: Maximum Safe Operating Area

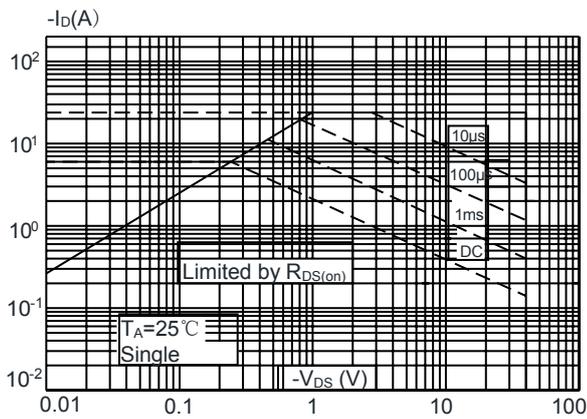


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

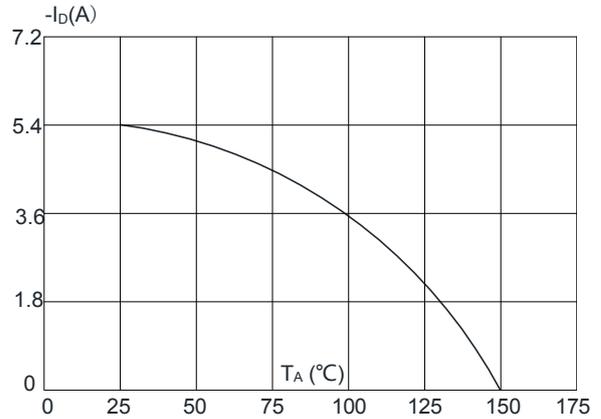
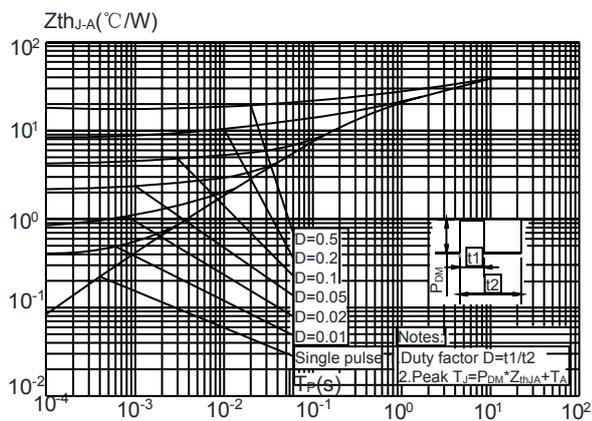
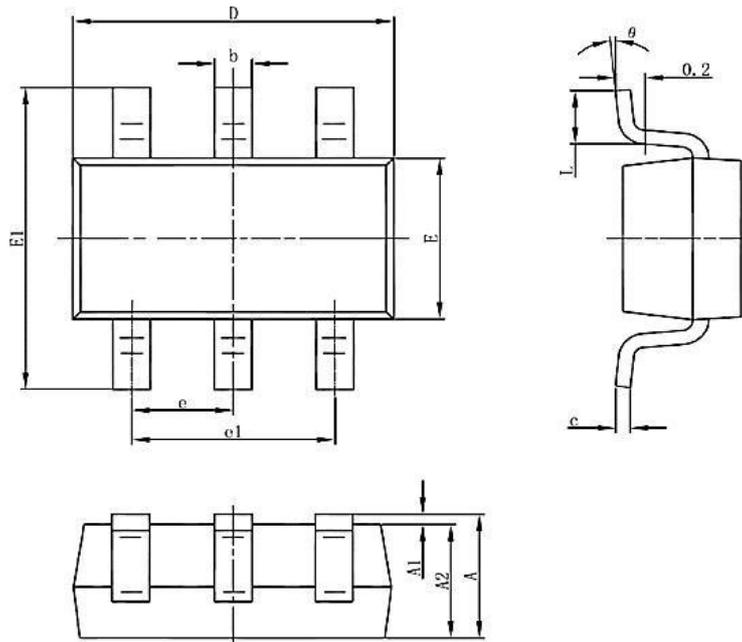


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



Package Mechanical Data-SOT23-6L-Double



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
C	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950 (BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0	8	0	8