

## Features

- Split Gate Trench MOSFET technology
- Excellent package for heat dissipation
- High density cell design for low  $R_{DS(ON)}$

## Applications

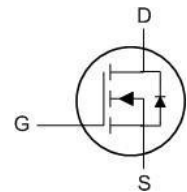
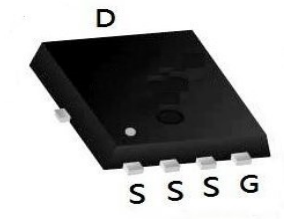
- DC-DC Converters
- Power management functions
- Synchronous-rectification applications

## Product Summary



| BVDSS | RDSON | ID  |
|-------|-------|-----|
| 60V   | 12mΩ  | 30A |

## PDFN3333-8L Pin Configuration



## Absolute Maximum Ratings

| Symbol                      | Parameter                                      | Rating     | Units            |
|-----------------------------|------------------------------------------------|------------|------------------|
| $V_{DS}$                    | Drain-Source Voltage                           | 60         | V                |
| $V_{GS}$                    | Gate-Source Voltage                            | $\pm 20$   | V                |
| $I_D@T_C=25^\circ\text{C}$  | Continuous Drain Current, $V_{GS} @ 10V^{1,6}$ | 30         | A                |
| $I_D@T_C=100^\circ\text{C}$ | Continuous Drain Current, $V_{GS} @ 10V^{1,6}$ | 19         | A                |
| $I_{DM}$                    | Pulsed Drain Current <sup>2</sup>              | 141        | A                |
| EAS                         | Single Pulse Avalanche Energy <sup>3</sup>     | 33.8       | mJ               |
| $I_{AS}$                    | Avalanche Current                              | 13         | A                |
| $P_D@T_C=25^\circ\text{C}$  | Total Power Dissipation <sup>4</sup>           | 27         | W                |
| $T_{STG}$                   | Storage Temperature Range                      | -55 to 150 | $^\circ\text{C}$ |
| $T_J$                       | Operating Junction Temperature Range           | -55 to 150 | $^\circ\text{C}$ |

## Thermal Data

| Symbol          | Parameter                                        | Typ. | Max. | Unit               |
|-----------------|--------------------------------------------------|------|------|--------------------|
| $R_{\theta JA}$ | Thermal Resistance Junction-Ambient <sup>1</sup> | ---  | 55   | $^\circ\text{C/W}$ |
| $R_{\theta JC}$ | Thermal Resistance Junction-Case <sup>1</sup>    | ---  | 4.6  | $^\circ\text{C/W}$ |

Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)

| Symbol                       | Parameter                                      | Conditions                                                       | Min. | Typ. | Max.      | Unit                       |
|------------------------------|------------------------------------------------|------------------------------------------------------------------|------|------|-----------|----------------------------|
| $BV_{DSS}$                   | Drain-Source Breakdown Voltage                 | $V_{GS}=0V$ , $I_D=250\mu A$                                     | 60   | ---  | ---       | V                          |
| $\Delta BV_{DSS}/\Delta T_J$ | $BV_{DSS}$ Temperature Coefficient             | Reference to $25^\circ\text{C}$ , $I_D=1\text{mA}$               | ---  | ---  | ---       | $V/^\circ\text{C}$         |
| $R_{DS(ON)}$                 | Static Drain-Source On-Resistance <sup>2</sup> | $V_{GS}=10V$ , $I_D=1A$                                          | ---  | 12   | 15        | $\text{m}\Omega$           |
|                              |                                                | $V_{GS}=4.5V$ , $I_D=1A$                                         | ---  | 15   | 19        |                            |
| $V_{GS(th)}$                 | Gate Threshold Voltage                         | $V_{GS}=V_{DS}$ , $I_D=250\mu A$                                 | 1    | 1.3  | 1.8       | V                          |
| $\Delta V_{GS(th)}$          | $V_{GS(th)}$ Temperature Coefficient           |                                                                  | ---  | ---  | ---       | $\text{mV}/^\circ\text{C}$ |
| $I_{DSS}$                    | Drain-Source Leakage Current                   | $V_{DS}=60V$ , $V_{GS}=0V$ , $T_J=25^\circ\text{C}$              | ---  | ---  | 1         | $\mu A$                    |
|                              |                                                | $V_{DS}=60V$ , $V_{GS}=0V$ , $T_J=100^\circ\text{C}$             | ---  | ---  | ---       |                            |
| $I_{GSS}$                    | Gate-Source Leakage Current                    | $V_{GS}=\pm 20V$ , $V_{DS}=0V$                                   | ---  | ---  | $\pm 100$ | nA                         |
| $g_{fs}$                     | Forward Transconductance                       | $V_{DS}=10V$ , $I_D=20A$                                         | ---  | 42   | ---       | S                          |
| $R_g$                        | Gate Resistance                                | $V_{DS}=0V$ , $V_{GS}=0V$ , $f=1\text{MHz}$                      | ---  | 1.2  | ---       | $\Omega$                   |
| $Q_g$                        | Total Gate Charge                              | $V_{DS}=30V$ , $V_{GS}=10V$ , $I_D=20$                           | ---  | 14   | ---       | nC                         |
| $Q_{gs}$                     | Gate-Source Charge                             |                                                                  | ---  | 2.5  | ---       |                            |
| $Q_{gd}$                     | Gate-Drain Charge                              |                                                                  | ---  | 2.9  | ---       |                            |
| $T_{d(on)}$                  | Turn-On Delay Time                             | $V_{GS}=10V$ , $V_{DD}=30V$ ,<br>$I_D=20A$ , $R_{GEN}=1.6\Omega$ | ---  | 5.7  | ---       | ns                         |
| $T_r$                        | Rise Time                                      |                                                                  | ---  | 5.1  | ---       |                            |
| $T_{d(off)}$                 | Turn-Off Delay Time                            |                                                                  | ---  | 14.8 | ---       |                            |
| $T_f$                        | Fall Time                                      |                                                                  | ---  | 4.6  | ---       |                            |
| $C_{iss}$                    | Input Capacitance                              | $V_{DS}=30V$ , $V_{GS}=0V$ , $f=1\text{MHz}$                     | ---  | 738  | ---       | pF                         |
| $C_{oss}$                    | Output Capacitance                             |                                                                  | ---  | 225  | ---       |                            |
| $C_{rss}$                    | Reverse Transfer Capacitance                   |                                                                  | ---  | 13.5 | ---       |                            |

## Diode Characteristics

| Symbol   | Parameter                                | Conditions                                       | Min. | Typ. | Max. | Unit |
|----------|------------------------------------------|--------------------------------------------------|------|------|------|------|
| $I_S$    | Continuous Source Current <sup>1,4</sup> | $V_G=V_D=0V$ , Force Current                     | ---  | ---  | 30   | A    |
| $V_{SD}$ | Diode Forward Voltage <sup>2</sup>       | $V_{GS}=0V$ , $I_S=20A$ , $T_J=25^\circ\text{C}$ | ---  | ---  | 1.2  | V    |
| $t_{rr}$ | Reverse Recovery Time                    | $I_F=13A$ , $di/dt=100A/\mu s$                   | ---  | 24   | ---  | nS   |
| $Q_{rr}$ | Reverse Recovery Charge                  | $\mu s$ , $T_J=25^\circ\text{C}$                 | ---  | 9.6  | ---  | nC   |

Notes:

1. Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}=150^\circ\text{C}$ .
2. The test condition is  $V_{DD}=25V$ ,  $V_{GS}=10V$ ,  $L=0.4\text{mH}$ ,  $I_{AS}=13A$ .
3. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
4. The data tested by pulsed, pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$ .
5. This value is guaranteed by design hence it is not included in the production test.

### Typical Characteristics

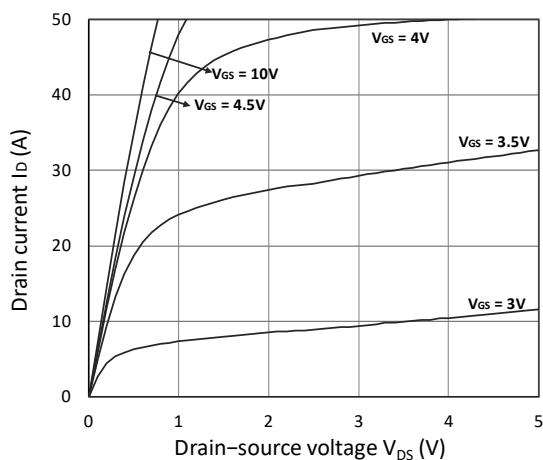


Figure 1. Output Characteristics

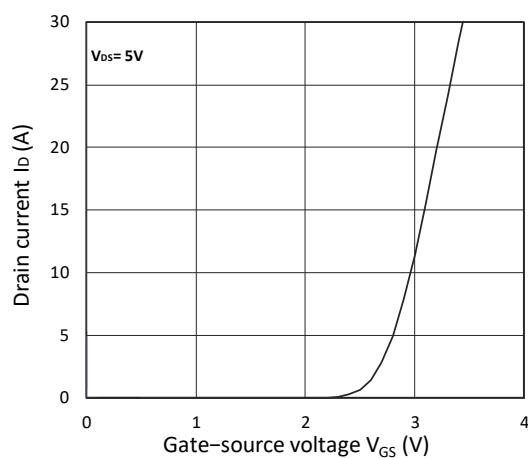


Figure 2. Transfer Characteristics

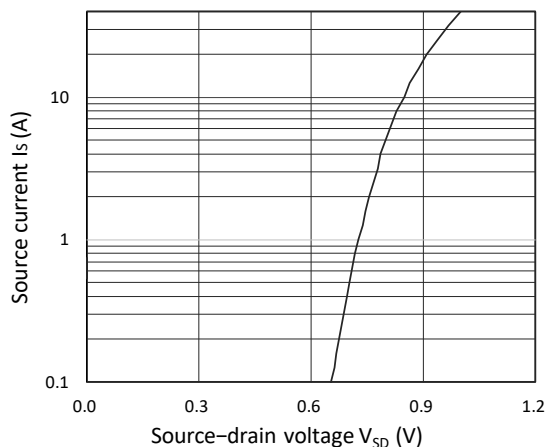


Figure 3. Forward Characteristics of Reverse

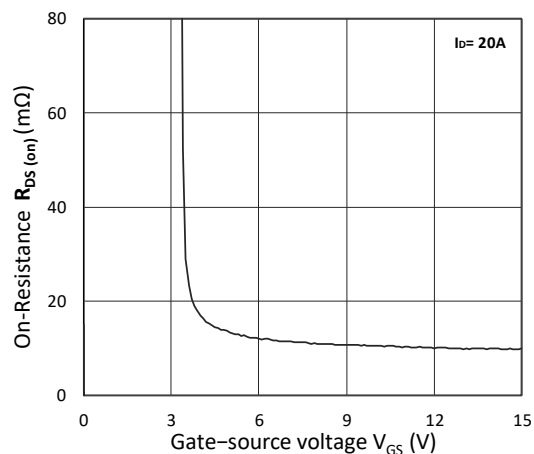


Figure 4.  $R_{DS(ON)}$  vs.  $V_{GS}$

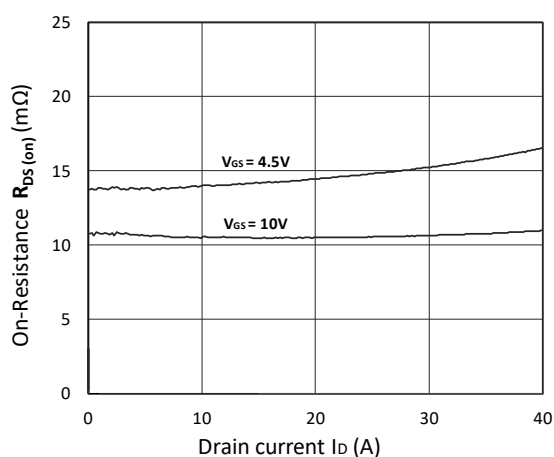


Figure 5.  $R_{DS(ON)}$  vs.  $I_D$

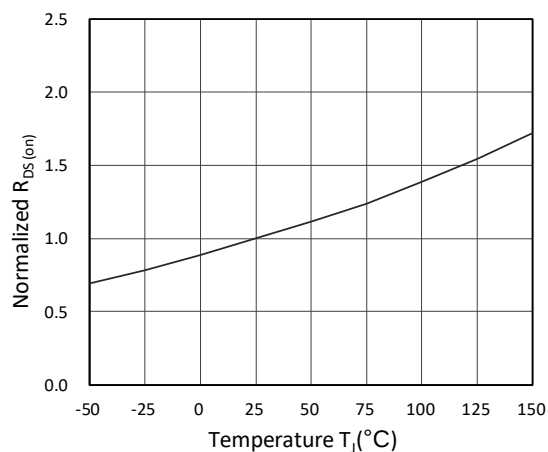


Figure 6. Normalized  $R_{DS(ON)}$  vs. Temperature

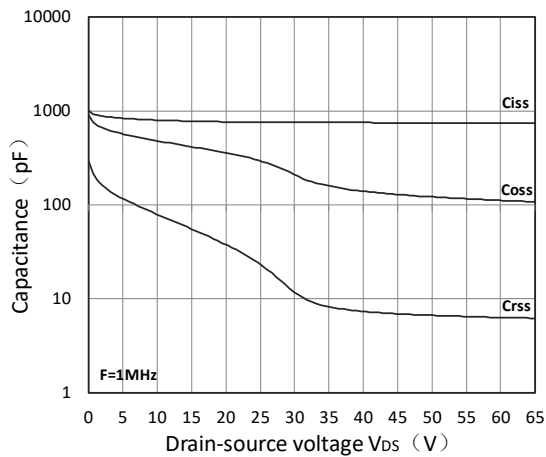


Figure 7. Capacitance Characteristics

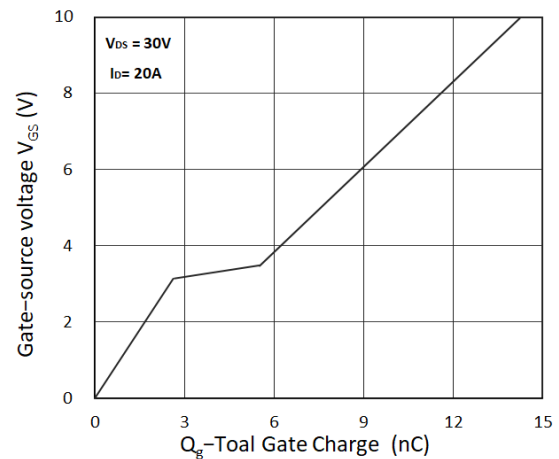


Figure 8. Gate Charge Characteristics

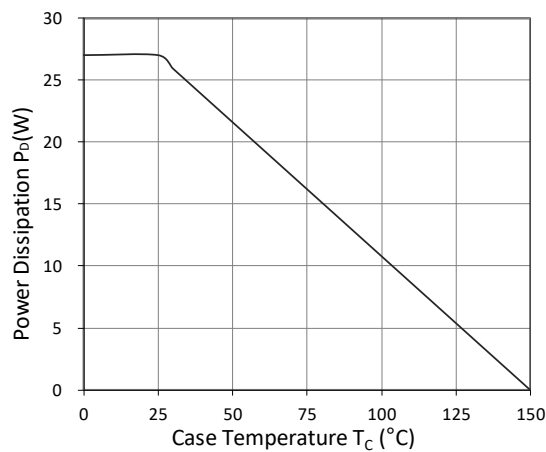


Figure 9. Power Dissipation

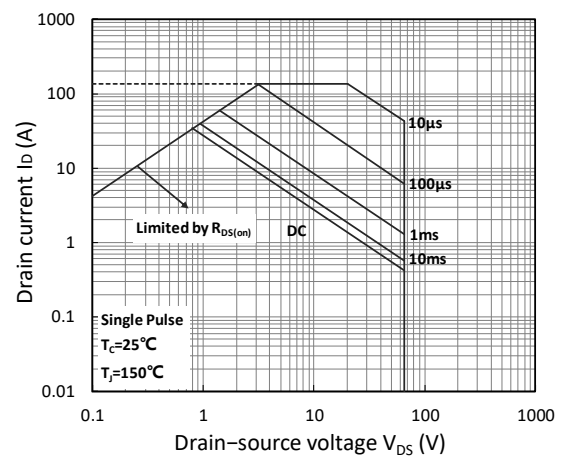


Figure 10. Safe Operating Area

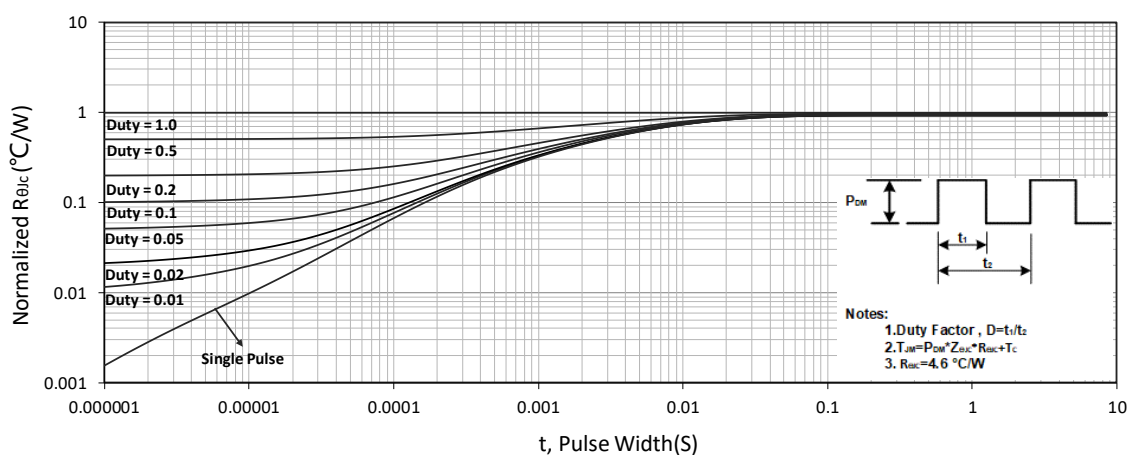
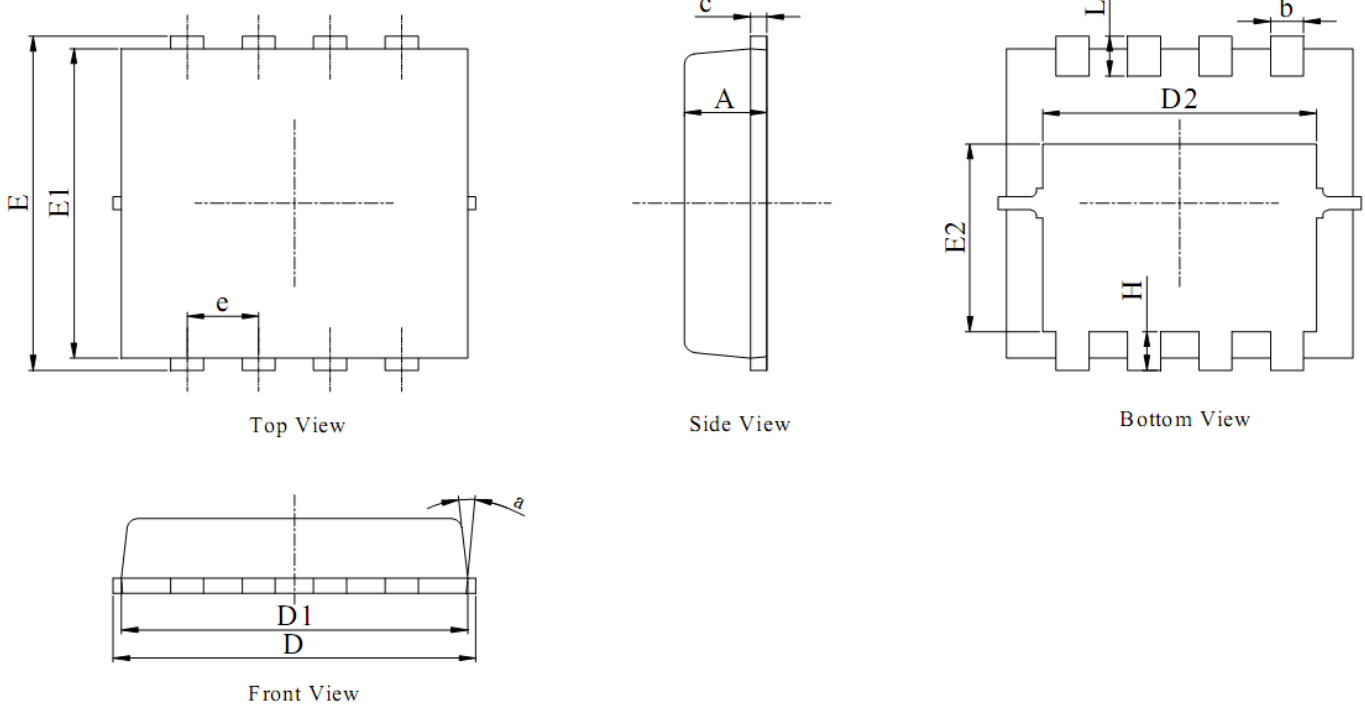


Figure 11. Normalized Maximum Transient Thermal Impedance

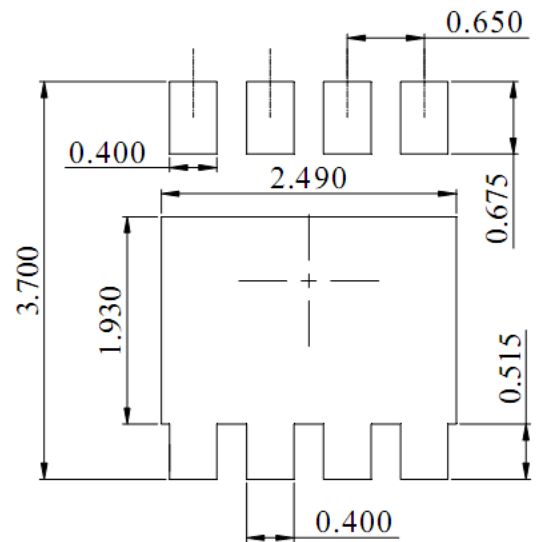
### Package Mechanical Data-PDFN3333-8L-Single



#### NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M,1994.
2. ALL DIMENSIONS IN MILLIMETER (ANGLE IN DEGREE).
3. DIMENSIONS D1 AND E1 DO NOT INCLUDE MOLD FLASH PROTRUSIONS OR GATE BURRS.

| DIM. | MILLIMETER |      |      |
|------|------------|------|------|
|      | MIN.       | NOM. | MAX. |
| A    | 0.70       | 0.75 | 0.80 |
| b    | 0.25       | 0.30 | 0.35 |
| c    | 0.10       | 0.20 | 0.25 |
| D    | 3.00       | 3.15 | 3.25 |
| D1   | 2.95       | 3.05 | 3.15 |
| D2   | 2.39       | 2.49 | 2.59 |
| E    | 3.20       | 3.30 | 3.40 |
| E1   | 2.95       | 3.05 | 3.15 |
| E2   | 1.70       | 1.80 | 1.90 |
| e    | 0.65 BSC   |      |      |
| H    | 0.30       | 0.40 | 0.50 |
| L    | 0.25       | 0.40 | 0.50 |
| a    | ---        | ---  | 15°  |



DIMENSIONS:MILLIMETERS