

**Features**

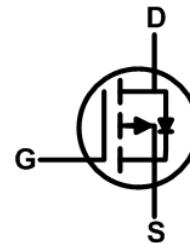
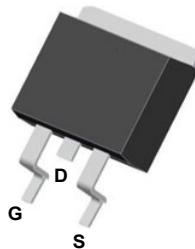
- Split Gate Trench MOSFET technology
- Excellent package for heat dissipation
- High density cell design for low  $R_{DS(ON)}$

**Product Summary**

BVDSS	RDS(on)	ID
-60V	7.3mΩ	-80A

**Applications**

- DC-DC Converters
- Power management functions
- Synchronous-rectification applications

**TO&\* Pin Configuration****Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-60	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^{1,6}$	-100	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^{1,6}$	-70	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	-440	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	960	mJ
$I_{AS}$	Avalanche Current	---	A
$P_D @ T_C = 25^\circ C$	Total Power Dissipation <sup>4</sup>	200	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

**Thermal Data**

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	---	80	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	---	0.69	°C/W

Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=-250\mu\text{A}$	-60	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$\text{BV}_{\text{DSS}}$ Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_D=-1\text{mA}$	---	---	---	$\text{V}/^\circ\text{C}$
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{\text{GS}}=-10\text{V}$ , $I_D=-15\text{A}$	---	7.3	9.2	$\text{m}\Omega$
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D=-250\mu\text{A}$	-1.6	-1.8	-2.3	V
$\Delta V_{\text{GS}(\text{th})}$	$V_{\text{GS}(\text{th})}$ Temperature Coefficient		---	---	---	$\text{mV}/^\circ\text{C}$
$I_{\text{DS}(\text{SS})}$	Drain-Source Leakage Current	$V_{\text{DS}}=-60\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	-1	$\text{uA}$
		$V_{\text{DS}}=-60$ , $V_{\text{GS}}=0\text{V}$ , $T_J=100^\circ\text{C}$	---	---	-100	
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=-5\text{V}$ , $I_D=-20\text{A}$	50	---	---	S
$R_g$	Gate Resistance	$V_{\text{DS}}=0\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	2	---	$\Omega$
$Q_g$	Total Gate Charge	$V_{\text{DS}}=-10\text{V}$ , $V_{\text{GS}}=-10\text{V}$ , $I_D=20\text{mA}$	---	56	---	nC
$Q_{\text{gs}}$	Gate-Source Charge		---	11	---	
$Q_{\text{gd}}$	Gate-Drain Charge		---	9	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{GS}}=-10\text{V}$ , $V_{\text{DD}}=-30\text{V}$ , $R_G=3\Omega$ , $I_D=-15\text{A}$	---	4.5	---	ns
$T_r$	Rise Time		---	2.5	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	14.5	---	
$T_f$	Fall Time		---	3.5	---	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=-10\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	3060	---	pF
$C_{\text{oss}}$	Output Capacitance		---	620	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	20	---	

## Diode Characteristics

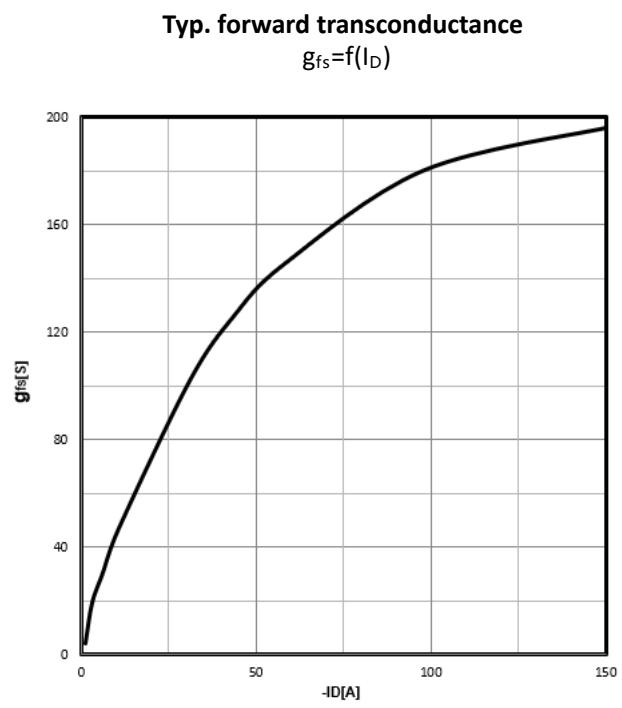
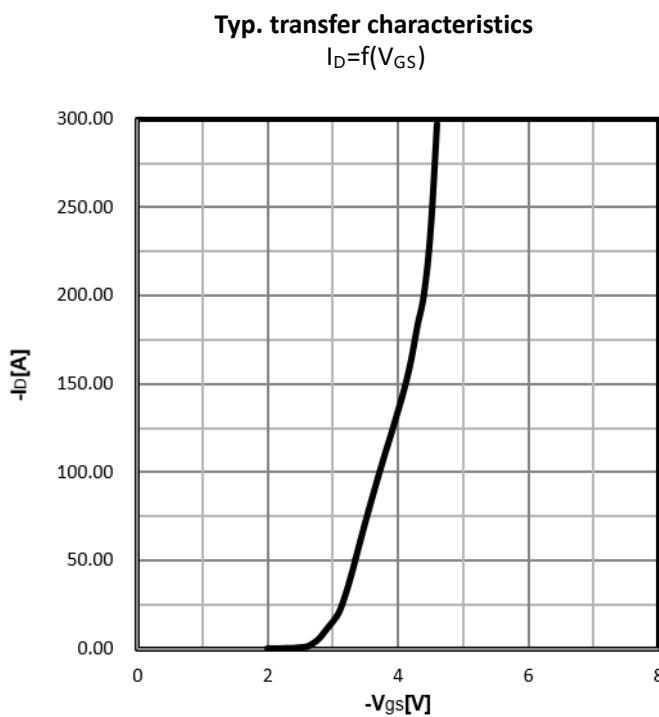
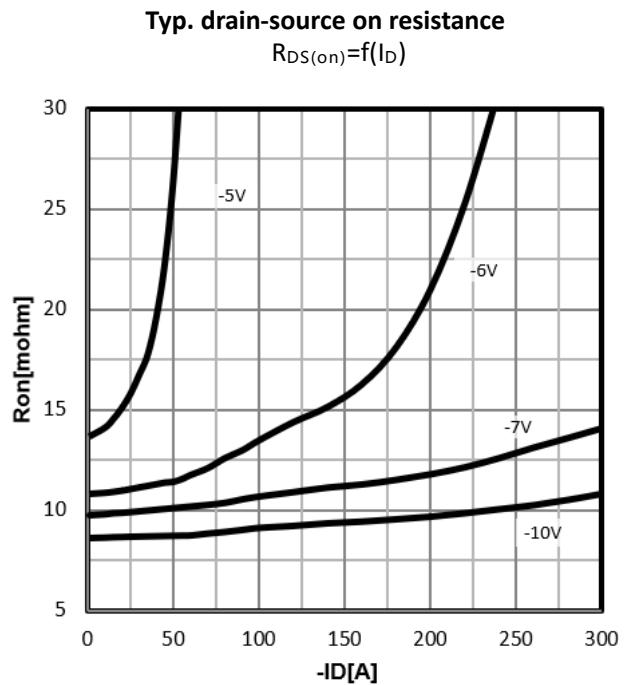
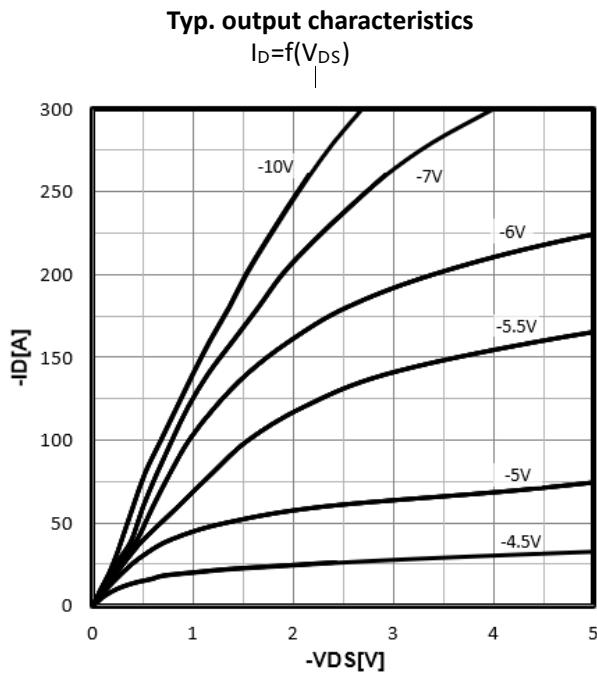
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current <sup>1,4</sup>	$V_G=V_D=0\text{V}$ , Force Current	---	---	-80	A
$V_{\text{SD}}$	Diode Forward Voltage <sup>2</sup>	$V_{\text{GS}}=0\text{V}$ , $I_s=-1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	-1.5	V
$t_{\text{rr}}$	Reverse Recovery Time	$I_F=-1\text{A}$ , $dI/dt=100\text{A}/\mu\text{s}$ , $T_J=25^\circ\text{C}$	---	1	---	nS
$Q_{\text{rr}}$	Reverse Recovery Charge		---	1	---	nC

Note :

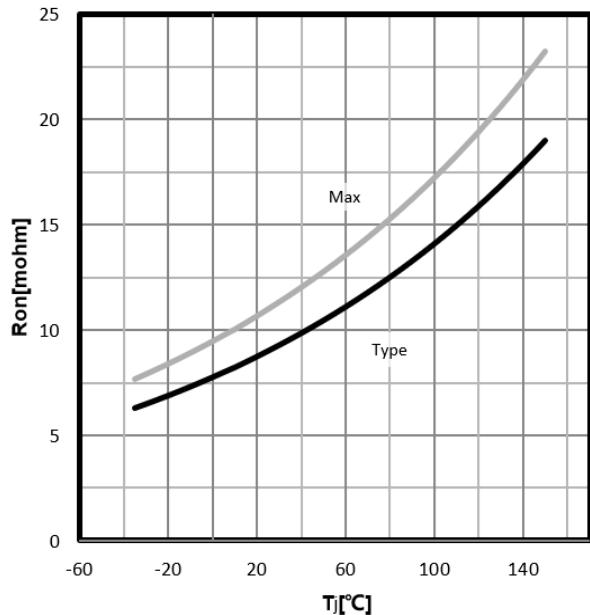
1 The data is tested by a surface mounted diode on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.2 The data is tested by a pulsed pulse width  $\leq 300\text{us}$  duty cycle  $\leq 2\%$ .

3 The EAS data shows Max. Rating at the test condition As VRM0, VDD=-30V, VGS=-10V, L=0.5mH

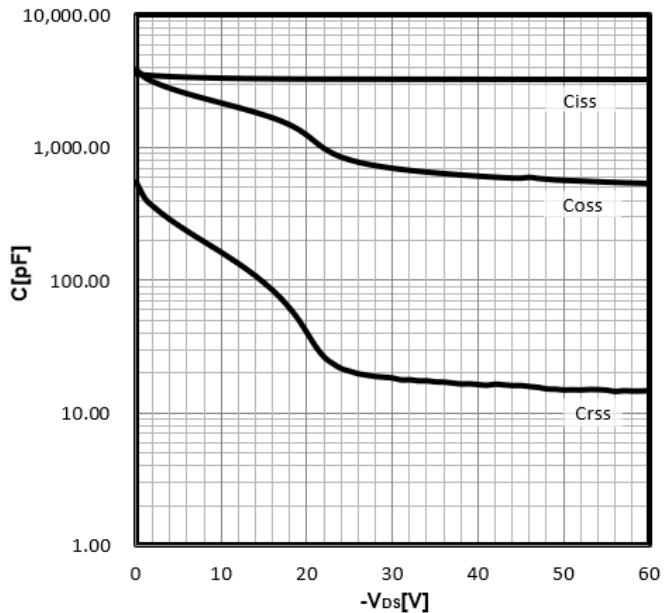
4 The power dissipation is limited by  $50^\circ\text{C}$  junction temperature5 The data is theoretically the same as  $I_{\text{DSS}}$  and  $I_{\text{DMA}}$ . In real applications it should be limited by total power dissipation.

**Characteristics Curve:**

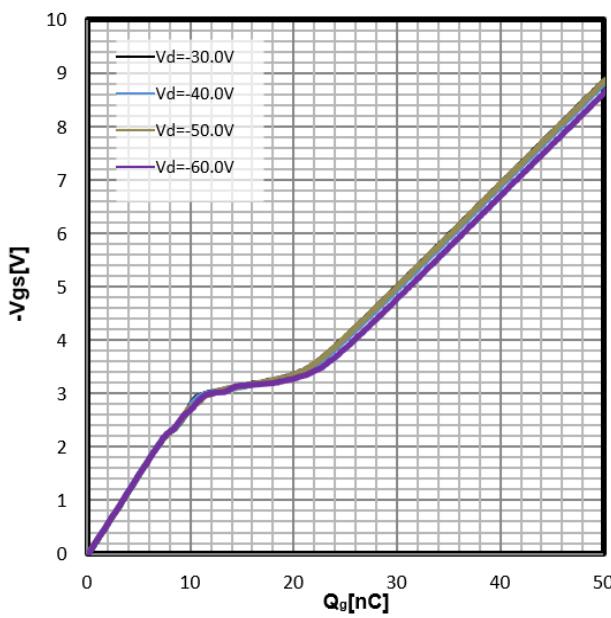
**Drain-source on-state resistance**  
 $R_{DS(on)} = f(T_j)$ ;  $I_D = -20A$ ;  $V_{GS} = -10V$



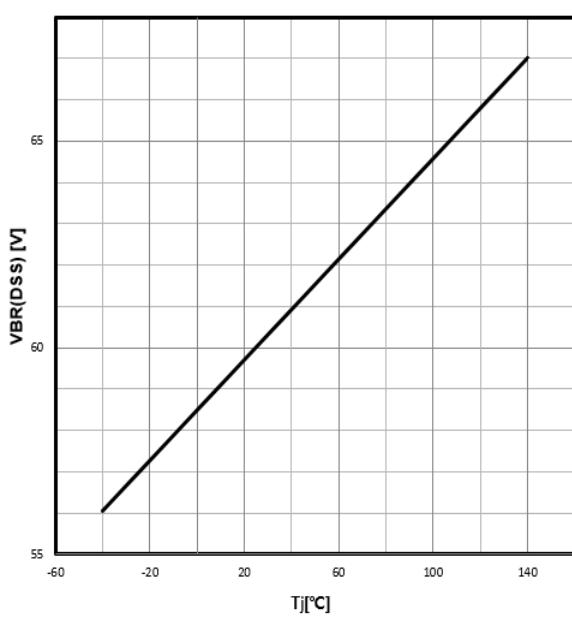
**Typ. capacitances**  
 $C = f(V_{DS})$ ;  $V_{GS} = 0V$ ;  $f = 1MHz$



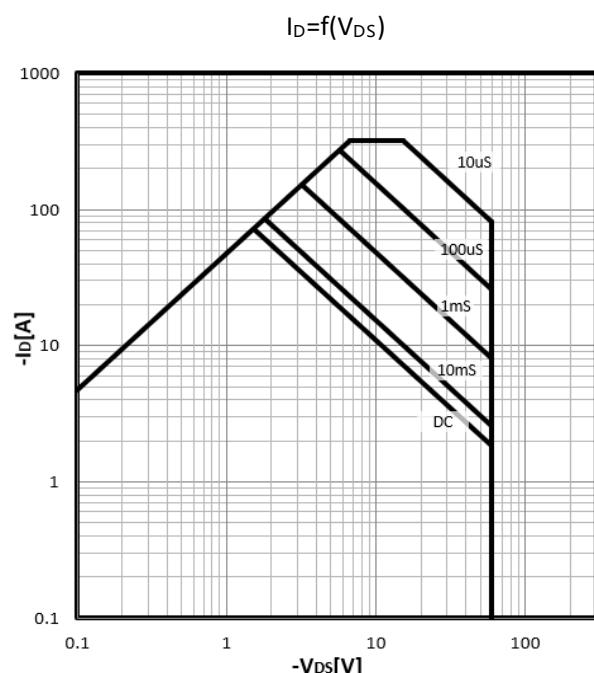
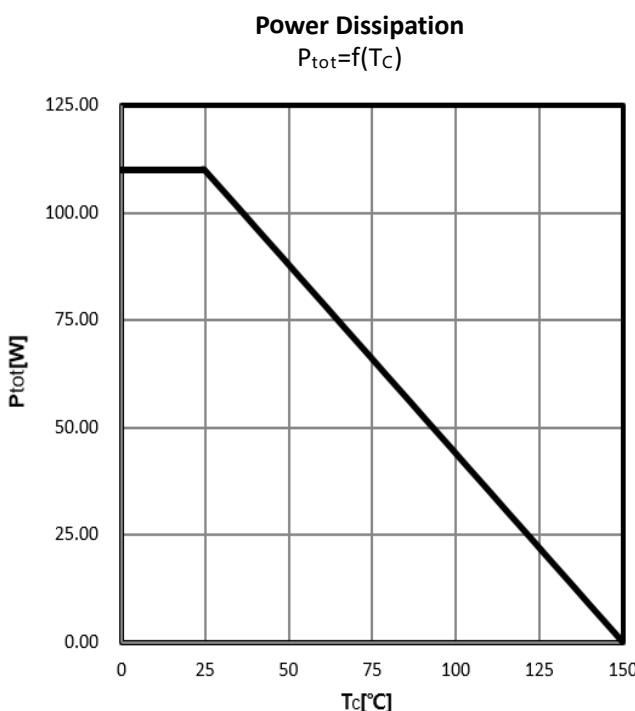
**Typ. gate charge**  
 $V_{GS} = f(Q_{gate})$ ;  $I_D = -20A$



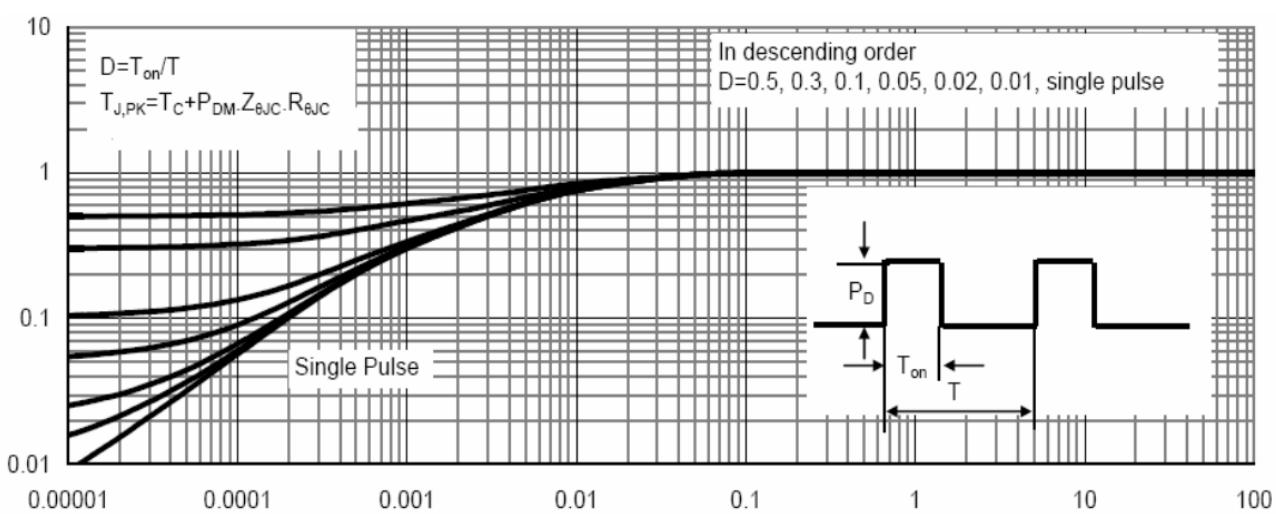
**Drain-source breakdown voltage**  
 $V_{BR(DSS)} = f(T_j)$ ;  $I_D = -250\mu A$



## P-Ch 60V Fast Switching MOSFETs

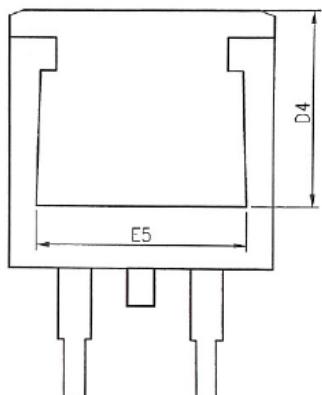
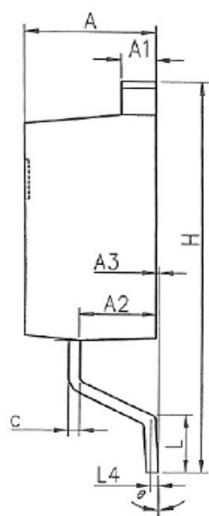
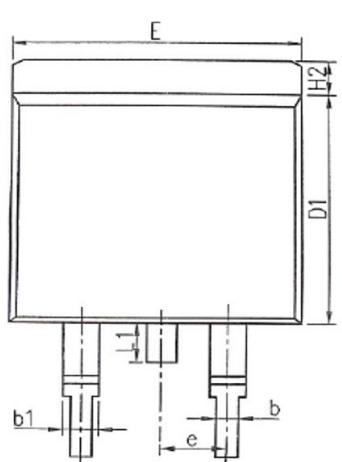
**Max. transient thermal impedance**

$Z_{thJC}=f(t_p)$



## Mechanical Dimensions for TO-263

## COMMON DIMENSIONS



SYMBOL	MM	
	MIN	MAX
A	4.37	4.89
A1	1.17	1.42
A2	2.20	2.90
A3	0.00	0.25
b	0.70	0.96
b1	1.17	1.47
c	0.28	0.60
D1	8.45	9.30
D4	6.60	-
E	9.80	10.40
E5	7.06	-
e	2.54BSC	
H	14.70	15.70
H2	1.07	1.47
L	2.00	2.80
L1	-	1.75
L4	0.254BSC	
θ	0°	9°